

Tentative Schedule – Fall 2017

| Day | Date | Topic(s) | Approximate Text Sections | Homework Problems Due |
|------------|---------------------|---|----------------------------------|------------------------------|
| T | August 22 | Introduction | 1.1 – 1.4, 2.3 | |
| R | August 24 | Kirchhoff's Laws, the Passive Sign Convention and Power Calculations | 2.4, 1.5 | |
| T | August 29 | Resistors, Ohm's Law, Series and Parallel Equivalent Resistance | 2.2, 2.5, 2.6 | 1 – 3 |
| R | August 31 | Voltage and Current Divider Circuits | 2.5, 2.6 | 4 – 6 |
| T | September 05 | Nodal Analysis | 3.2 | 7 – 9 |
| R | September 07 | Nodal Analysis (continued) | 3.3 | 10 – 12 |
| T | September 12 | Mesh Analysis | 3.4 | 13 – 15 |
| R | September 14 | Mesh Analysis (continued) | 3.5 | 16 – 18 |
| T | September 19 | Review | | |
| R | September 21 | Exam #1 [one-sided 8½"×11" page of notes and calculator allowed] | | |
| T | September 26 | Linearity and Superposition | 4.2, 4.3 | |
| R | September 28 | Thévenin/Norton Theorems, Source Transformation, Maximum Power Transfer | 4.4 – 4.6, 4.8 | 19 – 21 |
| T | October 03 | Controlled/Dependent Sources | 1.6 | 22 – 24 |
| R | October 05 | Operational Amplifiers | 5.1 – 5.3 | 25 – 27 |
| T | October 10 | Operational Amplifier Circuits | 5.4 – 5.8 | 28 – 30 |
| R | October 12 | Computer-Aided DC Circuit Analysis with PSpice | 3.8, 4.9, 5.9 | 31 – 33 |
| T | October 17 | Capacitors | 6.2, 6.3 | 34 – 36 |
| R | October 19 | Analog Computers | 6.6.1, 6.6.3 | 37 – 39 |
| T | October 24 | Review | | |
| R | October 26 | Exam #2 [one-sided 8½"×11" page of notes and calculator allowed] | | |
| T | October 30 | Inductors | 6.4, 6.5 | |
| R | November 02 | First-Order Transient Circuit Analysis | 7.2, 7.3 | 40 – 42 |
| T | November 07 | Transient Circuit Analysis with PSpice | 7.8, 8.9 | 43 – 45 |
| R | November 09 | First-Order Transient Circuit Analysis (continued) | 7.5, 7.6 | 46 – 48 |
| T | November 14 | First-Order Transient Circuit Analysis (continued) | 7.7 | 49 – 51 |
| R | November 16 | Second-Order Transient Circuit Analysis | 8.2 – 8.4 | 52 – 54 |
| T | November 21 | *** Fall Recess *** | | |
| R | November 23 | *** Fall Recess *** | | |
| T | November 28 | Second-Order Transient Circuit Analysis (continued) | 8.5, 8.6 | 55 – 57 |
| R | November 30 | Second-Order Transient Circuit Analysis (continued) | 8.7 | 58 – 60 |
| T | December 05 | Review | | |
| R | December 07 | Review | | |
| R | December 14 | Final Exam (7:30AM – 9:30AM) [open-book, notes and calculator allowed] | | |

Actual classroom discussion may vary slightly from this schedule.

Class normally meets Tuesday & Thursday, 9:30AM – 10:45AM, in LIBR 32 (Pocatello) and CHE 208 (Idaho Falls).

Exams, including the Final Exam, will be in LIBR 32 (Pocatello) and TAB 115 (Idaho Falls).