

EE 2240
Tentative Schedule – Fall 2021

Day	Date	Topic(s)
T	August 24	Introduction
R	August 26	Kirchhoff's Laws, Power Calculations, PSC
T	August 31	Resistors, Ohm's Law
R	September 02	Voltage and Current Division
T	September 07	Power Calculations
R	September 09	Simple Circuits
T	September 14	Simple Circuits
R	September 16	Nodal Analysis
T	September 21	Nodal Analysis (continued)
R	September 23	Nodal Analysis (continued)
T	September 28	Exam #1
R	September 30	Mesh Analysis
T	October 05	Mesh Analysis (continued)
R	October 07	LTspice & Computer-Aided DC Circuit Analysis
T	October 12	LTspice Examples
R	October 14	Linearity and Superposition
T	October 19	Thévenin/Norton Theorems, Maximum Power Transfer
R	October 21	Operational Amplifiers
T	October 26	Operational Amplifier Circuits
R	October 28	Capacitors
T	November 02	Operational Amplifier Circuits (continued)
R	November 04	Exam #2
T	November 09	Analog Computers
R	November 11	Inductors
T	November 16	Source-Free First-Order Transient Circuit Analysis
R	November 18	First-Order Transient Circuit Analysis with Sources
T	November 23	Fall Recess
R	November 25	Fall Recess
T	November 30	First-Order Transient Circuit Analysis with Switches
R	December 02	Transient Circuit Analysis with LTspice
T	December 07	Second-Order Transient Circuit Analysis
R	December 09	Second-Order Transient Circuit Analysis (continued)
T	December 14	Final Exam (7:30AM – 9:30AM)

Actual classroom discussion may vary slightly from this schedule.

Class normally meets Tuesday & Thursday, 8:00AM – 9:15AM,
in LIBR 36 (Pocatello) and CHE 313 (Idaho Falls).