

Fall 2017 - EE 4474/5574 Tentative Schedule

Day	Date	Topic	Homework Problems Due
M	August 21	Eclipse Day -- No Class	
W	August 23	Introduction	
F	August 25	Some OpAmp Building Blocks	
M	August 28	More OpAmp Building Blocks	
W	August 30	Log and Antilog Amplifiers	
F	September 1	An Analog Multiplier -----	1 - 5
M	September 4	Labor Day Holiday -- No Class	
W	September 6	SPICE Simulation	
F	September 8	SPICE Diode Models	
M	September 11	A Bandpass Filter	
W	September 13	Bode Plots with Matlab/Freemat	
F	September 15	Voltage Divider, Current Divider -----	6 - 10
M	September 18	Thévenin's Theorem, Norton Equivalent Circuit	
W	September 20	Superposition	
F	September 22	Reciprocity -----	11 - 15
M	September 25	Nodal Analysis	
W	September 27	Exam #1 -- In Class	
F	September 29	Two-Port Network Parameters	
M	October 2	More Two-Port Parameters -----	16 - 20
W	October 4	Even More Two-Port Parameters	
F	October 6	SNAP	
M	October 9	More SNAP	
W	October 11	Two-Port Parameters	
F	October 13	More Two-Port Parameters -----	21 - 25
M	October 16	Cascaded Two-Port Networks	
W	October 18	Two-Port Equivalent Circuits	
F	October 20	A Simulated Inductor Circuit -----	26 - 30
M	October 23	Simulated Inductor Circuits	
W	October 25	Network Synthesis from Immittance Functions	
F	October 27	Cauer Synthesis -----	31 - 35
M	October 30	Foster Synthesis	
W	November 1	Exam #2 -- In Class	
F	November 3	Positive Real Functions & Realization as an RLC Network	
M	November 6	Reactive Functions, LC Networks	
W	November 8	Network Synthesis Examples	

F	November 10	Filter Transformations -----	36 - 40
M	November 13	LP & HP Frequency Scaling	
W	November 15	BP Frequency Scaling	
F	November 17	Inductorless Filter Design -----	41 - 45
M	November 20	Fall Recess -- No Class	
W	November 22	Fall Recess -- No Class	
F	November 24	Fall Recess -- No Class	
M	November 27	Impedance Scaling	
W	November 29	State Equations, Solution by ODE Methods & SFG	
F	December 1	Continuation of Solution by SFG -----	46 - 50
M	December 4	More on SFG's	
W	December 6	Solution by State Transition Matrix	
F	December 8	Circuit Design from SFG	
M	December 11	Exam #3 -- 5:30PM - 6:30PM, REND 124	

Actual classroom discussion may vary slightly from this schedule.

Class normally meets Monday, Wednesday and Friday, 4:00PM - 4:50PM, in REND 124.